features of the present invention, its embodiments will be described in greater detail hereinbelow with reference to the accompanying drawings, in which:

Fig. 1 is a block diagram showing an exemplary general hardware setup of a waveform editing system in accordance with a first embodiment of the present invention;

Figs. 2A - 2C are diagrams explanatory of processing for generating inserting sections and coupled sections in the first embodiment;

Figs. 3A and 3B are diagrams explanatory of reproduction processing performed in the first embodiment;

Fig. 4 is a flow chart of to-be-reproduced-waveform-data generation processing performed in the first embodiment;

Figs. 5A and 5B are diagram showing waveforms before and after an unnecessary frequency band removal process performed in the first embodiment;

Fig. 6 is a flow chart of a process for determining default waveform data control points in an analysis mode of the first embodiment;

Figs. 7A - 7D are diagrams showing waveforms output from an absolute value acquisition process performed in the first embodiment;

Fig. 8 is a block diagram showing an example of circuitry equivalent to an algorithm of an envelope follower process performed in the first embodiment;

Figs. 9A - 9D are a block diagram showing an example